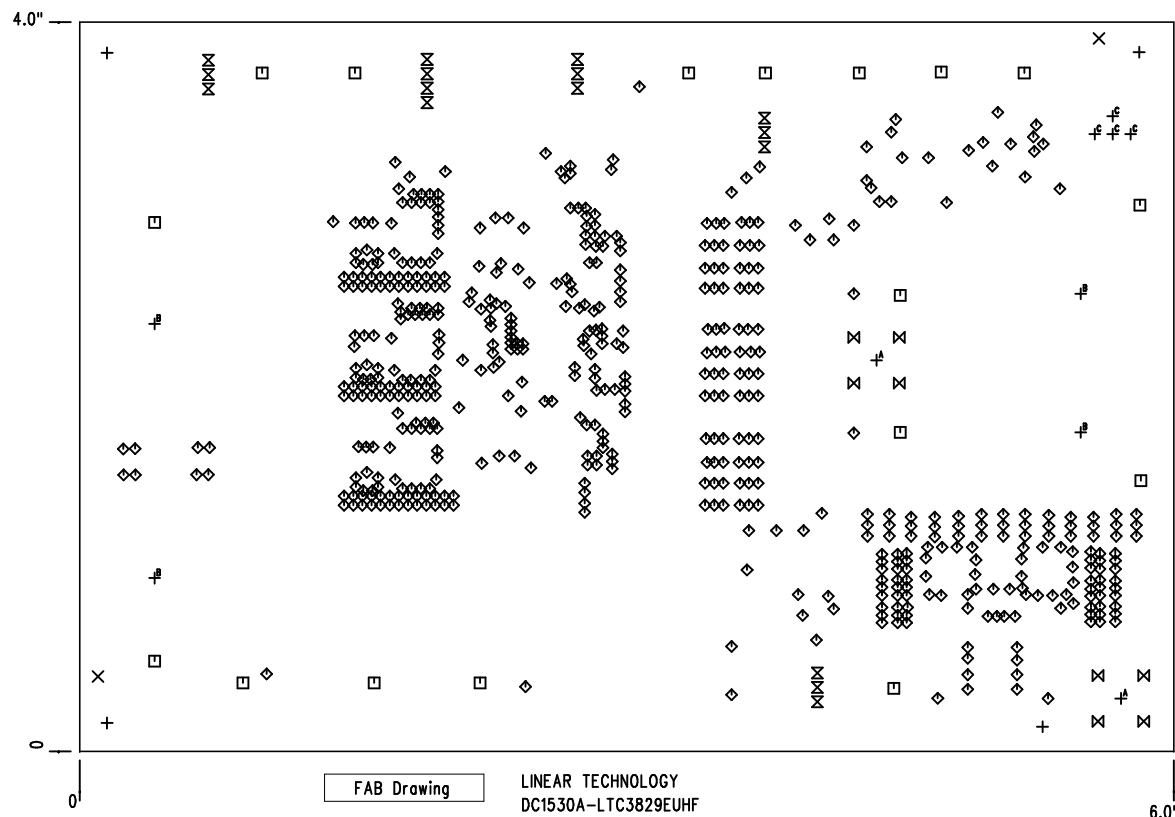


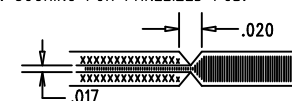
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	JIAN LI	03-22-10



LAYER 1 (COMPONENT SIDE)
 LAYER 2
 LAYER 3
 LAYER 4
 LAYER 5
 LAYER 6 (SOLDER SIDE)

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:

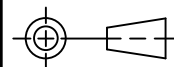


FAB Drawing

LINEAR TECHNOLOGY
 DC1530A-LTC3829EUHF
 HIGH CURRENT 3-PHASE SINGLE OUTPUT STEP-DOWN SUPPLY
 DATE: 03-22-10

SIZE	QTY	SYM	PLATED	TOL
0.185	4	+	NO	+/-0.003
0.07	2	X	NO	+/-0.003
0.094	17	□	YES	+/-0.003
0.01	553	◇	YES	+/-0.003
0.031	16	⊗	YES	+/-0.003
0.052	8	⊗	YES	+/-0.003
0.059	2	+ ^A	YES	+/-0.003
0.189	4	+ ^B	YES	+/-0.003
0.035	4	+ ^C	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 TOLERANCES:
 0.XX" = ±0.01"
 0.XXX" = ±0.005"
 INTERPRET DIM AND TOL
 PER ASME Y14.5M-1994
 THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	A.K.
APP ENG.	JIAN L.
SCALE = NONE	

LINEAR
TECHNOLOGY

1630 MCCARTHY BLVD
 MILPITAS, CA 95035
 PH: (408)432-1900
 www.linear.com
 LTC CONFIDENTIAL-
 FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
 HIGH CURRENT 3-PHASE
 SINGLE OUTPUT STEP-DOWN SUPPLY

SIZE	IC NO.	REV
N/A	LTC3829EUHF DEMO CIRCUIT 1530A	2
FILENAME: DC1530A-2.PCB	SHT 1 OF 1	